



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Luan Tran Et Al.

§ Group Art Unit: 2815

Serial No.: 10/059,727

§ Examiner: Jesse A. Fenty

Filed: January 29, 2002

§ Atty. Dkt. No.: MCT.0004C1US  
§ (97-0903.02)

For: Memory Cell Arrays

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

AMENDMENT UNDER 37 C.F.R. § 1.116

Please amend the application as set forth below.

**Amendments to the Claims** begins on page 2 of this paper.

**Remarks/Arguments** begin on page 6 of this paper.

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Date of Deposit:	11/18/2003
I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313.	
<i>Dawn L. Thomas</i>	
Dawn L. Thomas	